BeagleBone A3 Port 9 Multiplexer table - A3 Printable - Revision 1											
PORT	**PIN**	**PROC**	**NAME**	**MODE0**	**MODE1**	**MODE2**	**MODE3**	**MODE4**	**MODE**	**MODE6**	**MODE7**
P9_	1		GND								
P9_	2		GND								
P9_	3		DC_3.3V								
P9_	4		DC_3.3V								
P9_	5		VDD_5V								
P9_	6		VDD_5V								
P9_	7		SYS_5V								
P9_	8		SYS_5V								
P9_	9		PWR_BUT								
P9_	10	A10	SYS_RESETn	RESET_OUT							
P9_	11	T17	UART4_RXD	gpmc_wait0	mii2_crs	gpmc_csn4	rmii2_crs_dv	mmc1_sdcd		uart4_rxd_mux2	gpio0[30]
P9_	12	U18	GPIO1_28	gpmc_be1n	mii2_col	gpmc_csn6	mmc2_dat3	gpmc_dir		mcasp0_aclkr_mux3	gpio1[28]
P9_	13	U17	UART4_TXD	gpmc_wpn	mii2_rxerr	gpmc_csn5	rmii2_rxerr	mmc2_sdcd		uart4_txd_mux2	gpio0[31]
P9_	14	U14	EHRPWM1A	gpmc_a2	mii2_txd3	rgmii2_td3	mmc2_dat1	gpmc_a18		ehrpwm1A_mux1	gpio1[18]
P9_	15	R13	GPIO1_16	gpmc_a0	gmii2_txen	rmii2_tctl	mii2_txen	gpmc_a16		ehrpwm1_tripzone_input	gpio1[16]
P9_	16	T14	EHRPWM1B	gpmc_a3	mii2_txd2	rgmii2_td2	mmc2_dat2	gpmc_a19		ehrpwm1B_mux1	gpio1[19]
P9_	17	A16	I2C1_SCL	spi0_cs0	mmc2_sdwp	I2C1_SCL	ehrpwm0_synci				gpio0[5]
P9_	18	B16	I2C1_SDA	spi0_d1	mmc1_sdwp	I2C1_SDA	ehrpwm0_tripzone				gpio0[4]
P9_	19	D17	I2C2_SCL	uart1_rtsn	timer5	dcan0_rx	I2C2_SCL	spi1_cs1			gpio0[13]
P9_	20	D18	I2C2_SDA	uart1_ctsn	timer6	dcan0_tx	I2C2_SDA	spi1_cs0			gpio0[12]
P9_	21	B17	UART2_TXD	spi0_d0	uart2_txd	I2C2_SCL	ehrpwm0B			EMU3_mux1	gpio0[3]
P9_	22	A17	UART2_RXD	spi0_sclk	uart2_rxd	I2C2_SDA	ehrpwm0A			EMU2_mux1	gpio0[2]
P9_	23	V14	GPIO1_17	gpmc_a1	gmii2_rxdv	rgmii2_rxdv	mmc2_dat0	gpmc_a17		ehrpwm0_synco	gpio1[17]
P9_	24	D15	UART1_TXD	uart1_txd	mmc2_sdwp	dcan1_rx	I2C1_SCL				gpio0[15]
P9_	25	A14	GPIO3_21	mcasp0_ahclkx	eQEP0_strobe	mcasp0_axr3	mcasp1_axr1	EMU4_mux2			gpio3[21]
P9_	26	D16	UART1_RXD	uart1_rxd	mmc1_sdwp	dcan1_tx	I2C1_SDA				gpio0[14]
P9_	27	C13	GPIO3_19	mcasp0_fsr	eQEP0B_in	mcasp0_axr3	mcasp1_fsx	EMU2_mux2			gpio3[19]
P9_	28	C12	SPI1_CS0	mcasp0_ahclkr	ehrpwm0_synci	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out			gpio3[17]
P9_	29	B13	SPI1_D0	mcasp0_fsx	ehrpwm0B		spi1_d0	mmc1_sdcd_mux1			gpio3[15]
P9_	30	D12	SPI1_D1	mcasp0_axr0	ehrpwm0_tripzone		spi1_d1	mmc2_sdcd_mux1			gpio3[16]
P9_	31	A13	SPI1_SCLK	mcasp0_aclkx	ehrpwm0A		spi1_sclk	mmc0_sdcd_mux1			gpio3[14]
P9_	32		VADC								
P9_	33	C8	AIN4								
P9_	34		AGND								
P9_	35	A5	AIN6								
P9_	36	A5	AIN5								
P9_	37	B7	AIN2								
P9_	38	A7	AIN3								
P9_	39	B6	AIN0								
P9_	40	C7	AIN1								
P9_	41	D14	CLKOUT2	xdma_event_intr1		tclkin	clkout2				
P9_	42	C18	GPIO0_7	eCAP0_in_PWM0_out	uart3_txd	spi1_cs1	pr1_ecap0_ecap_capin_apwm_o				
P9_	43		GND								
P9_	44		GND								
P9_	45		GND								
P9_	46		GND								

Note: The multiplexer states can be modified by writing over the file in
/sys/kernel/debug/omap_mux/#name#
where #name# is the mode0 of the pin
Command Line example to set Port 9 Pin 14 to ehrpwm1A_mux1
echo 6 > /sys/kernel/debug/omap_mux/gpmc_a2

Source Based on data in BeagleBone system reference manual RevA3_1.0

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